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Feldman, M.B.; Moran, M.L.;
Software Engineering, IEEE Transactions on, Volume: 15, Issue: 3, March 1
Pages:305 - 313

[\[Abstract\]](#) [\[PDF Full-Text \(832 KB\)\]](#) IEEE JNL

2 Program synthesis: an automationist's view

McCartney, R.;
COMPON Spring '89. Thirty-Fourth IEEE Computer Society International Conference: Intellectual Leverage, Digest of Papers., 27 Feb.-3 March 1989
Pages:226 - 231

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3 Specifying Ada server tasks with executable formal grammars

Hemmendinger, D.;
Software Engineering, IEEE Transactions on, Volume: 16, Issue: 7, July 1994
Pages:741 - 754

[\[Abstract\]](#) [\[PDF Full-Text \(1184 KB\)\]](#) IEEE JNL

4 A layered architecture for real time applications

Loborg, P.; Torne, A.;
Real-Time Systems, 1995. Proceedings., Seventh Euromicro Workshop on, 14-16 June 1995
Pages:11 - 16

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